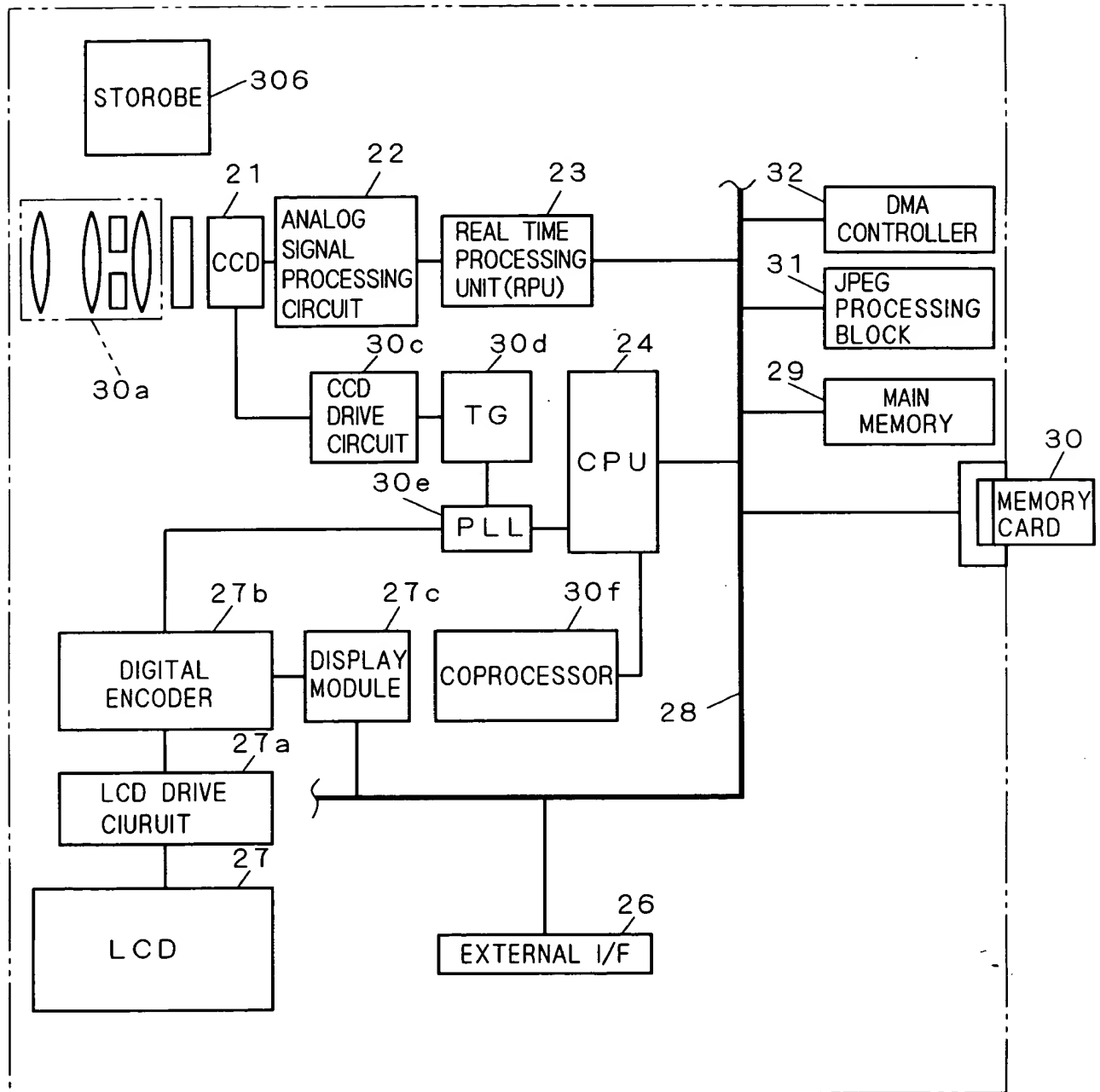
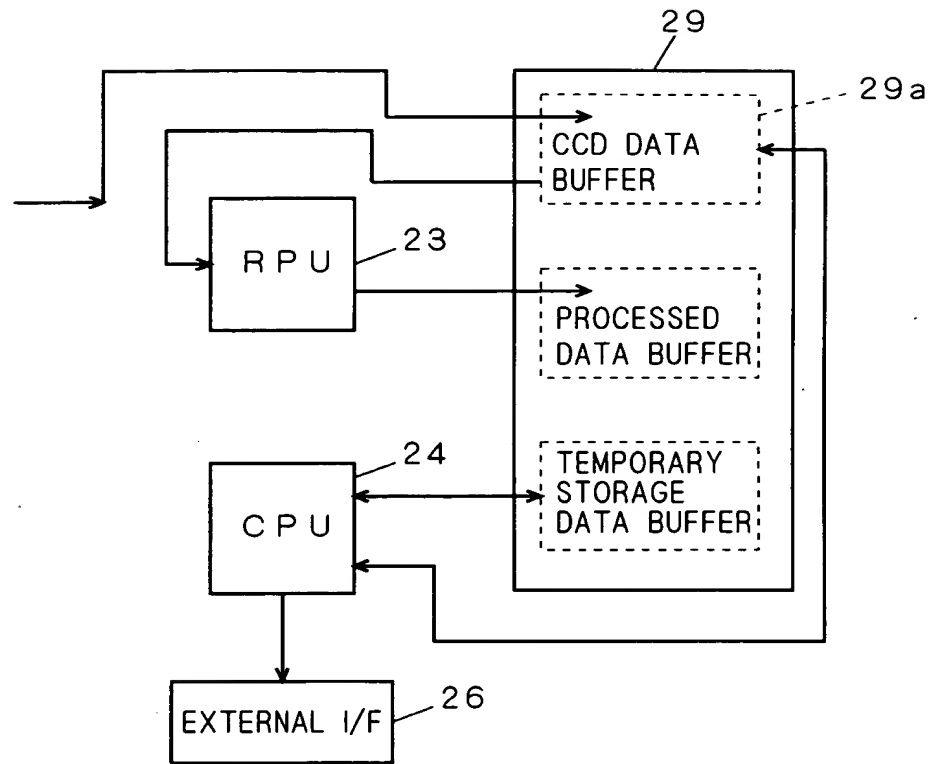


FIG. 1



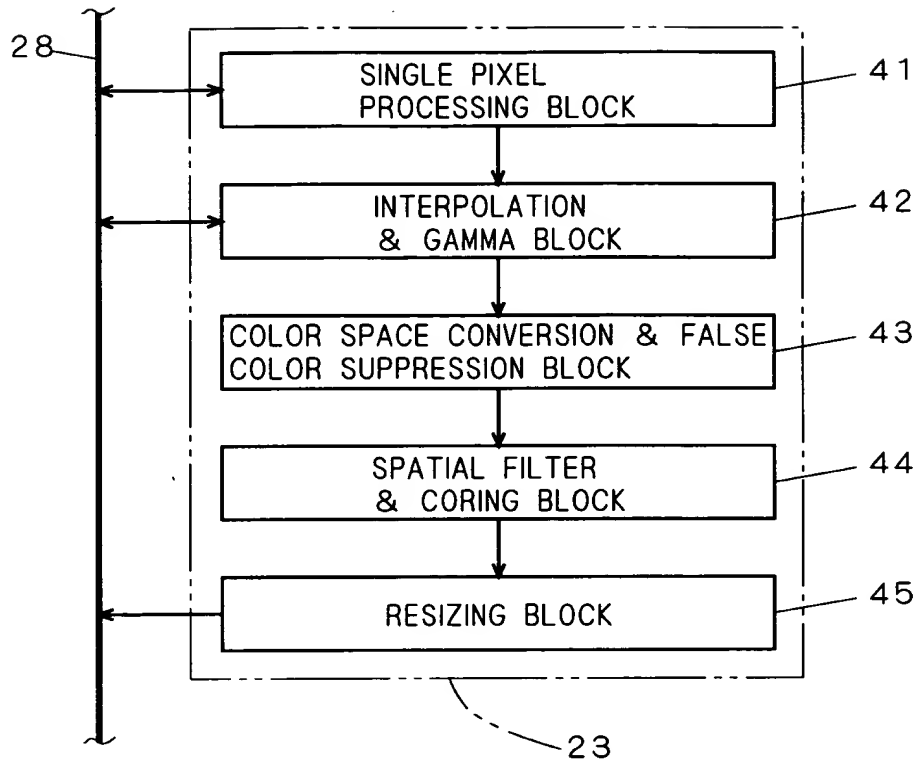
66E121 1455460

FIG. 2



045574.121309

FIG. 3



045974-121390

23

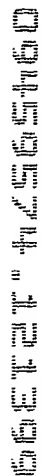
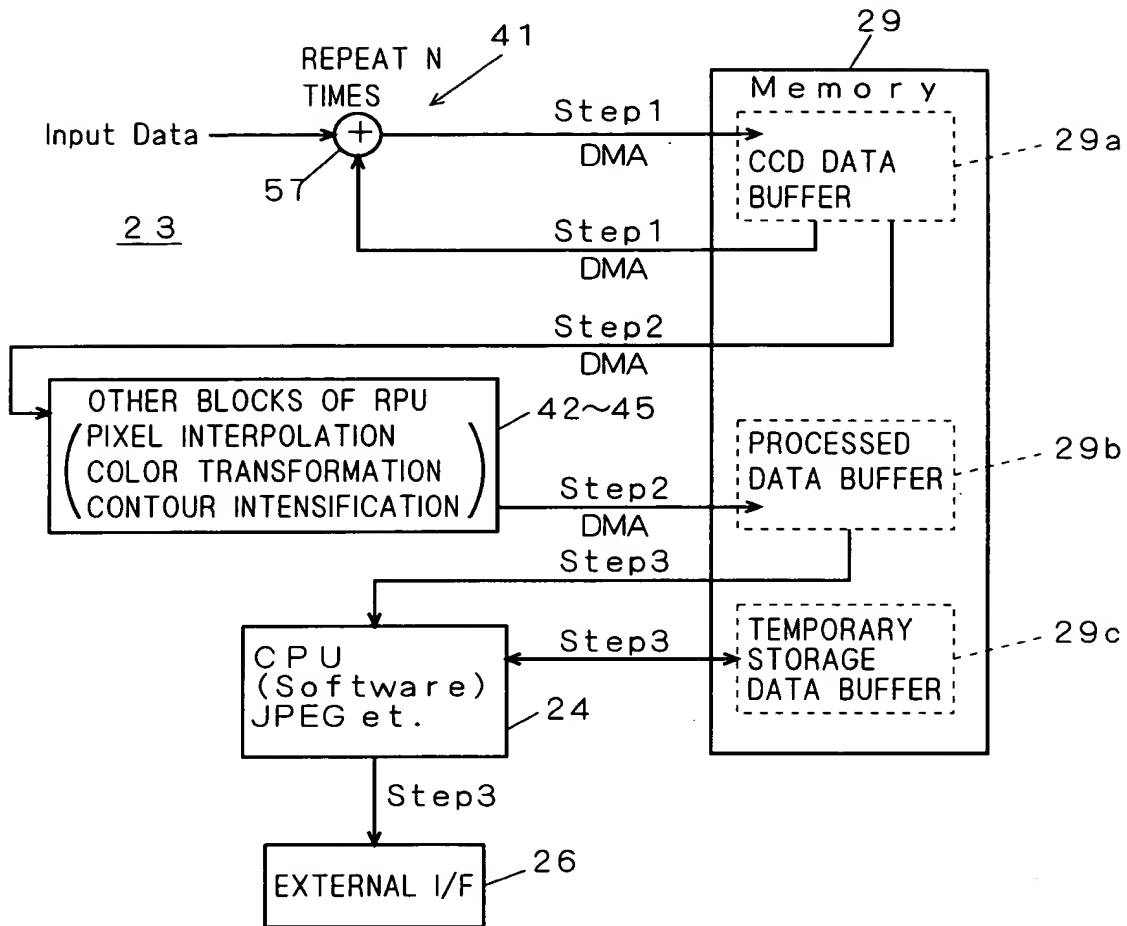


FIG. 5



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FIG. 6

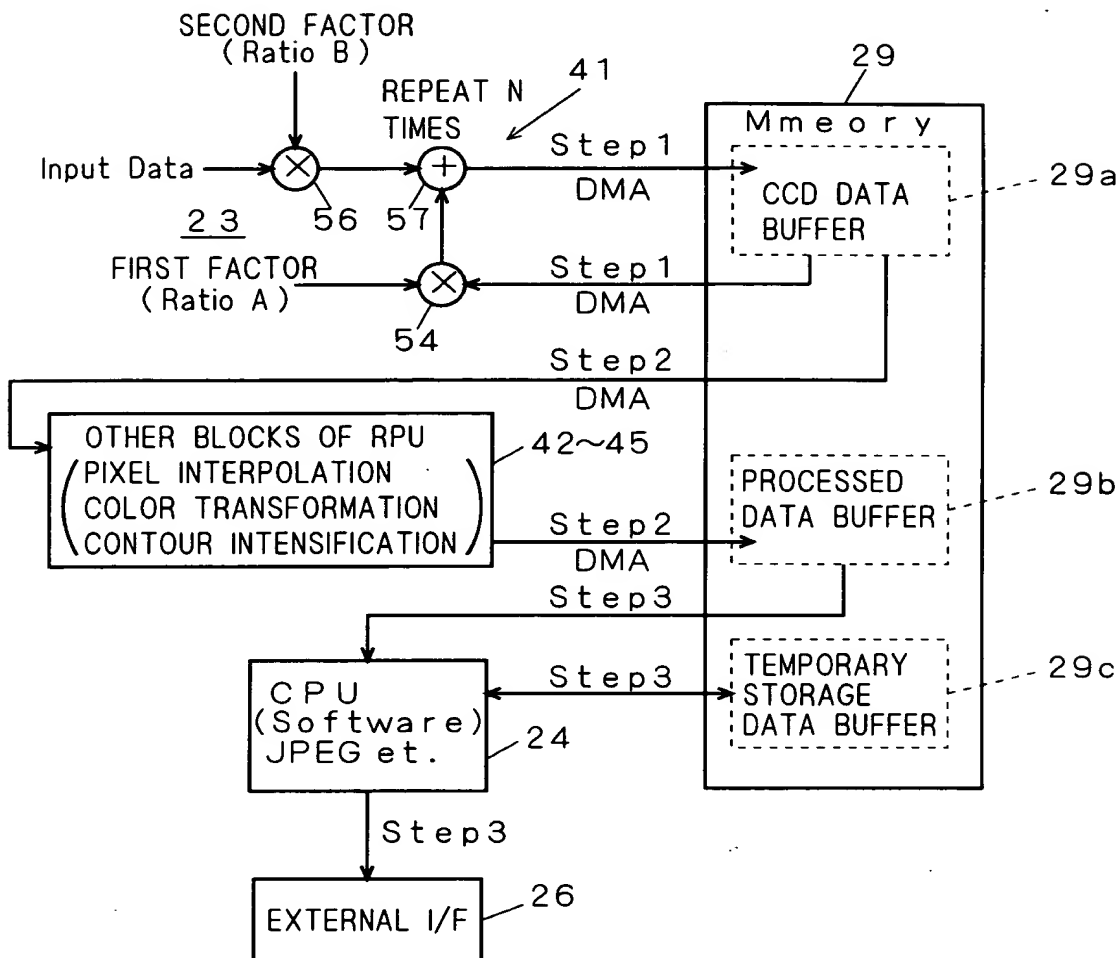


FIG. 7

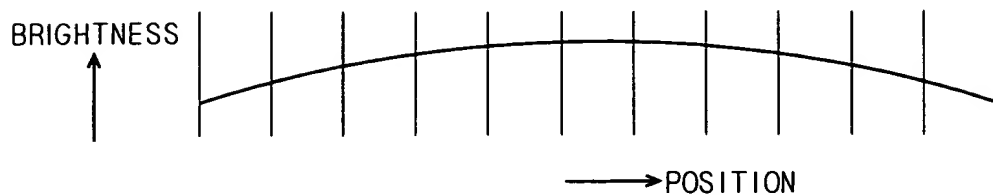
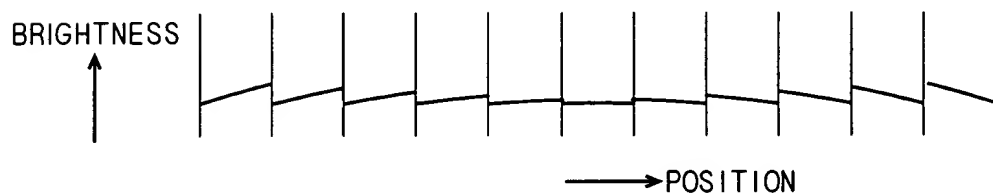


FIG. 8



09459574-121399

FIG. 9

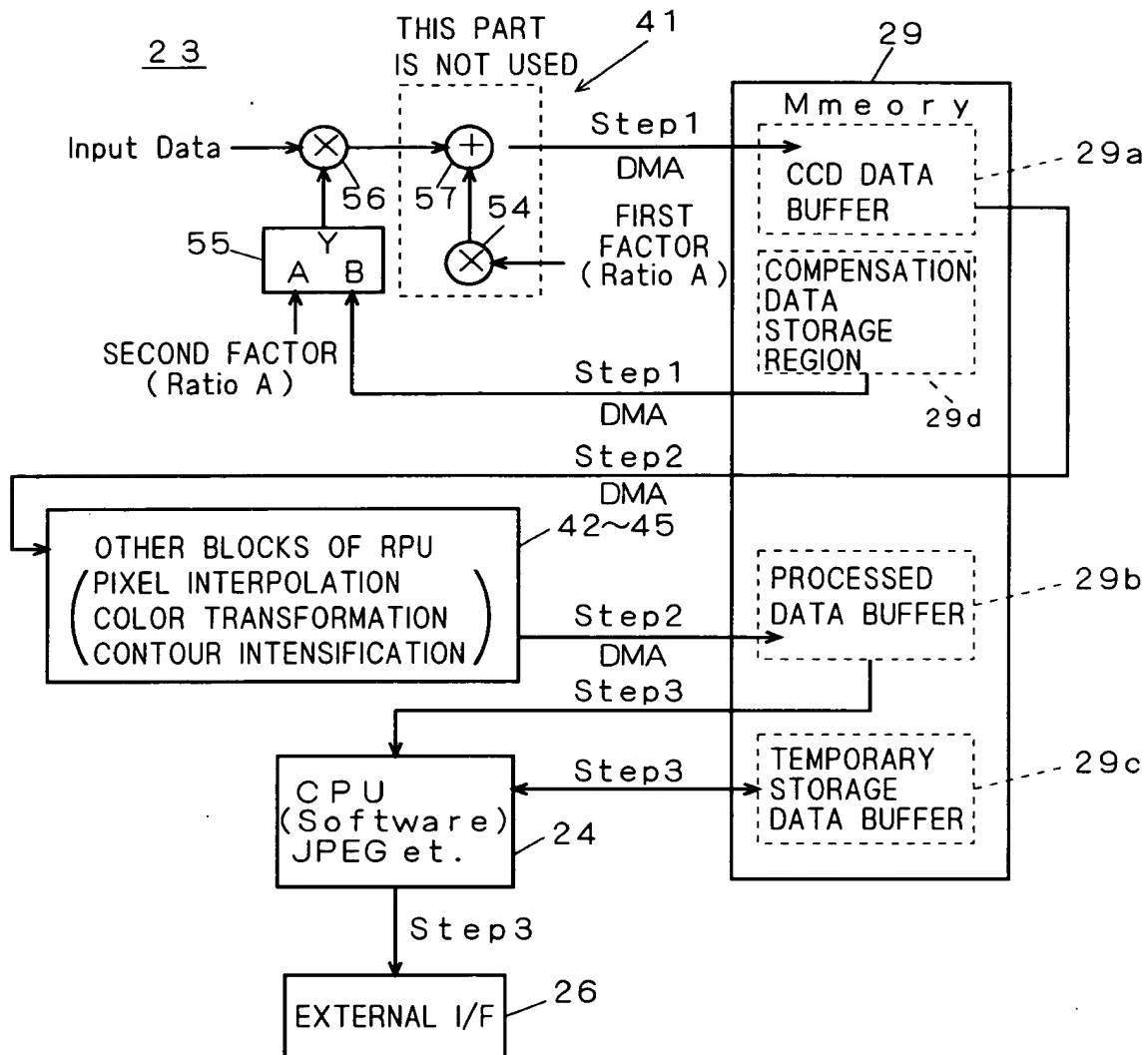




FIG. 10

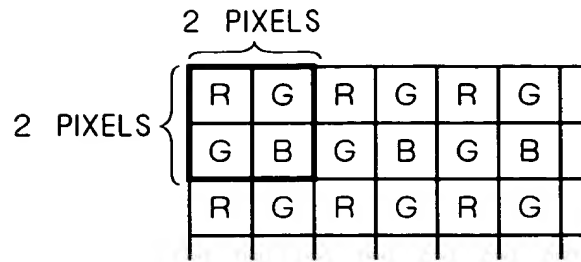


FIG. 11

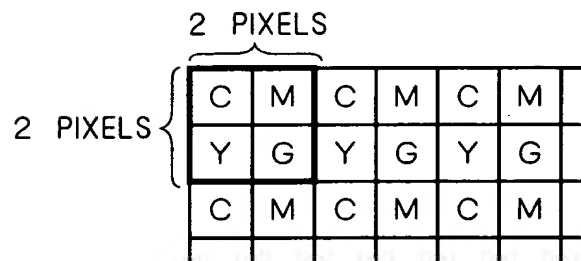


FIG. 12

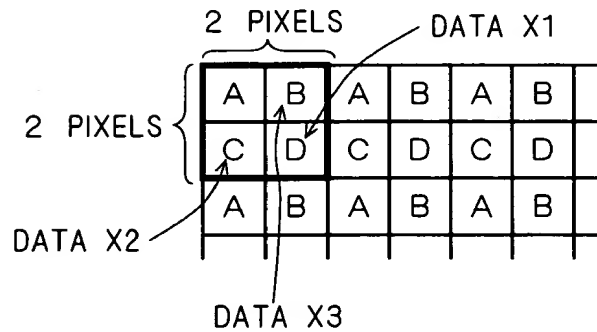
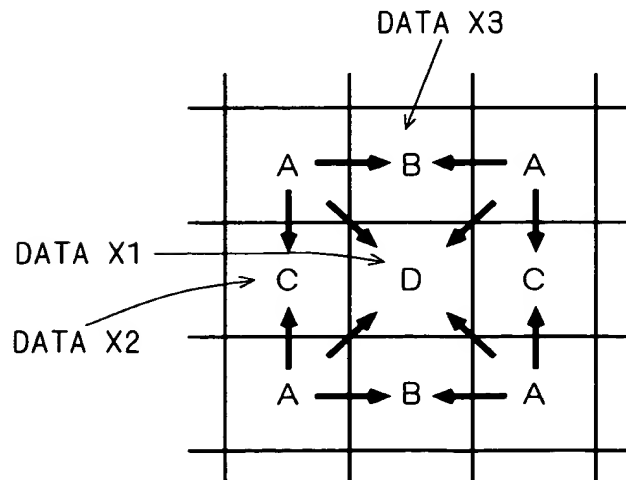


FIG. 13



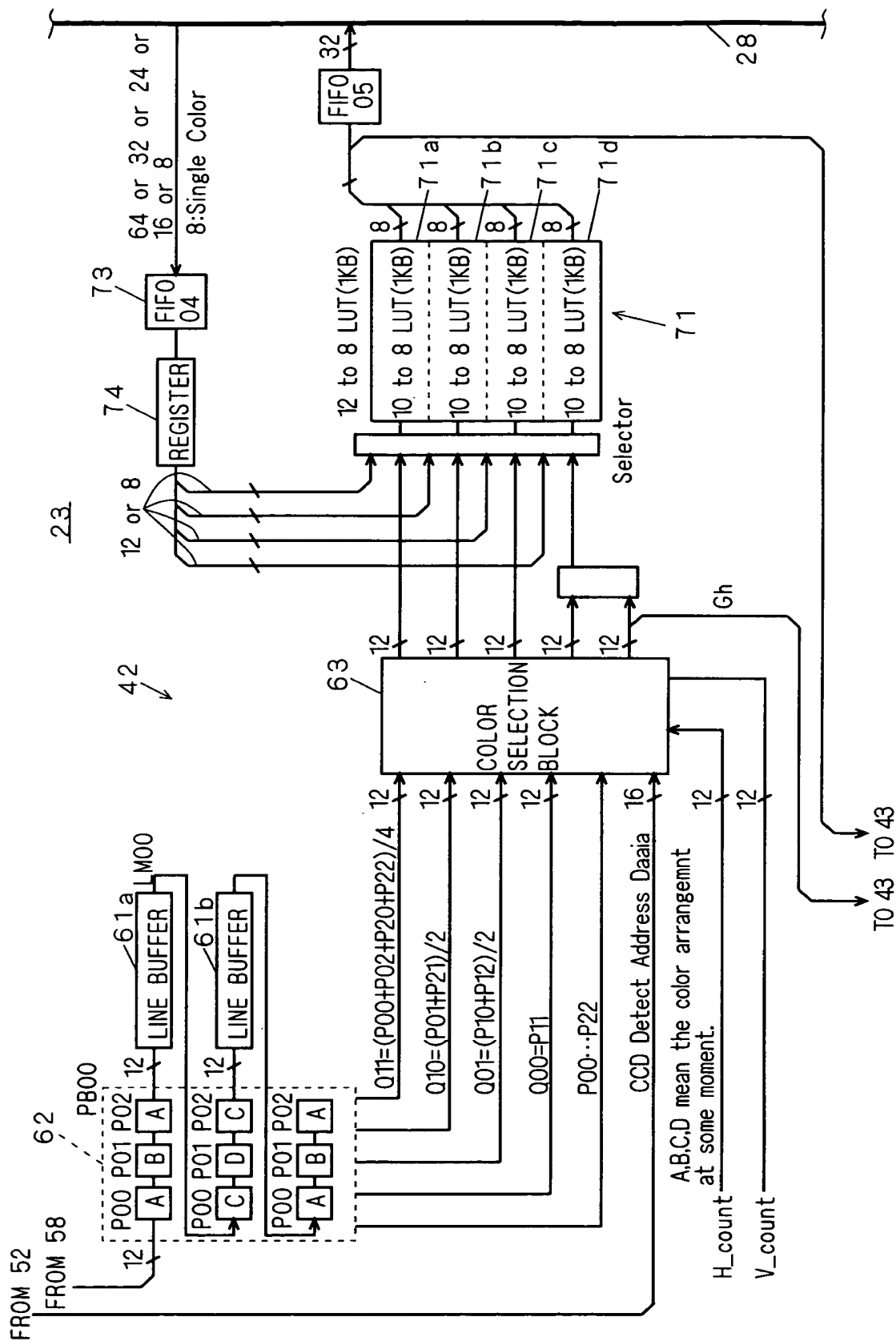


FIG. 15

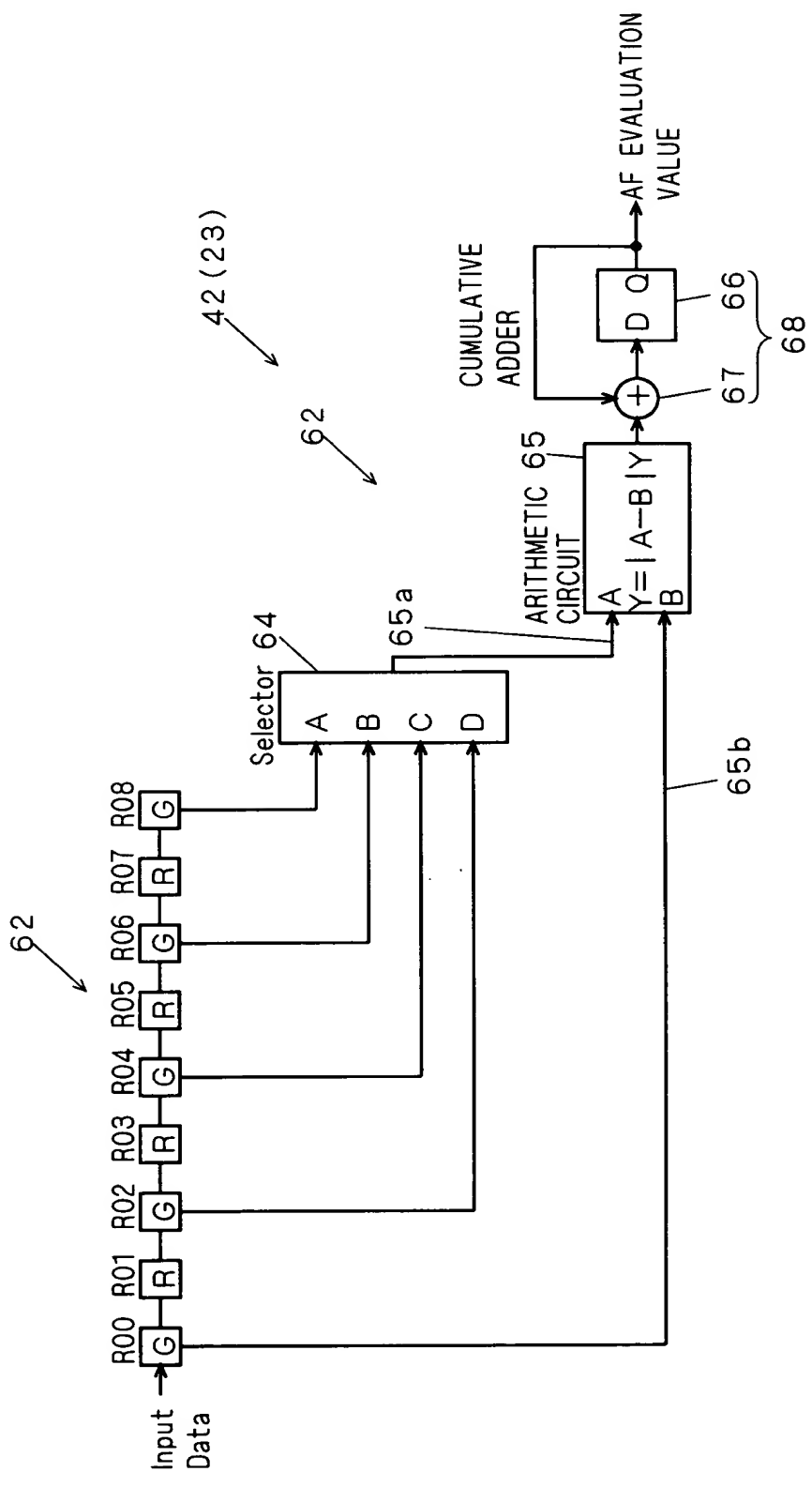


FIG. 16

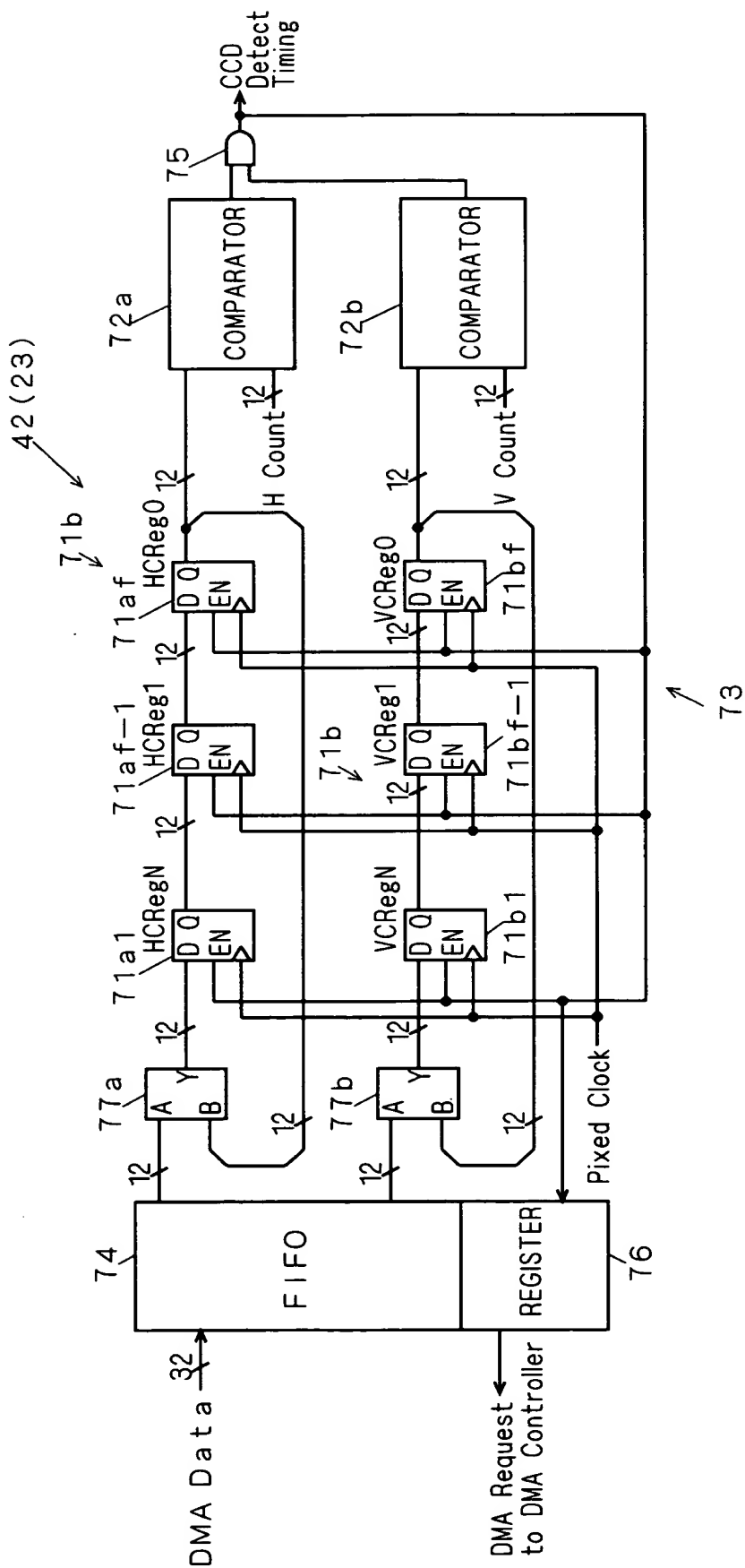


FIG. 17

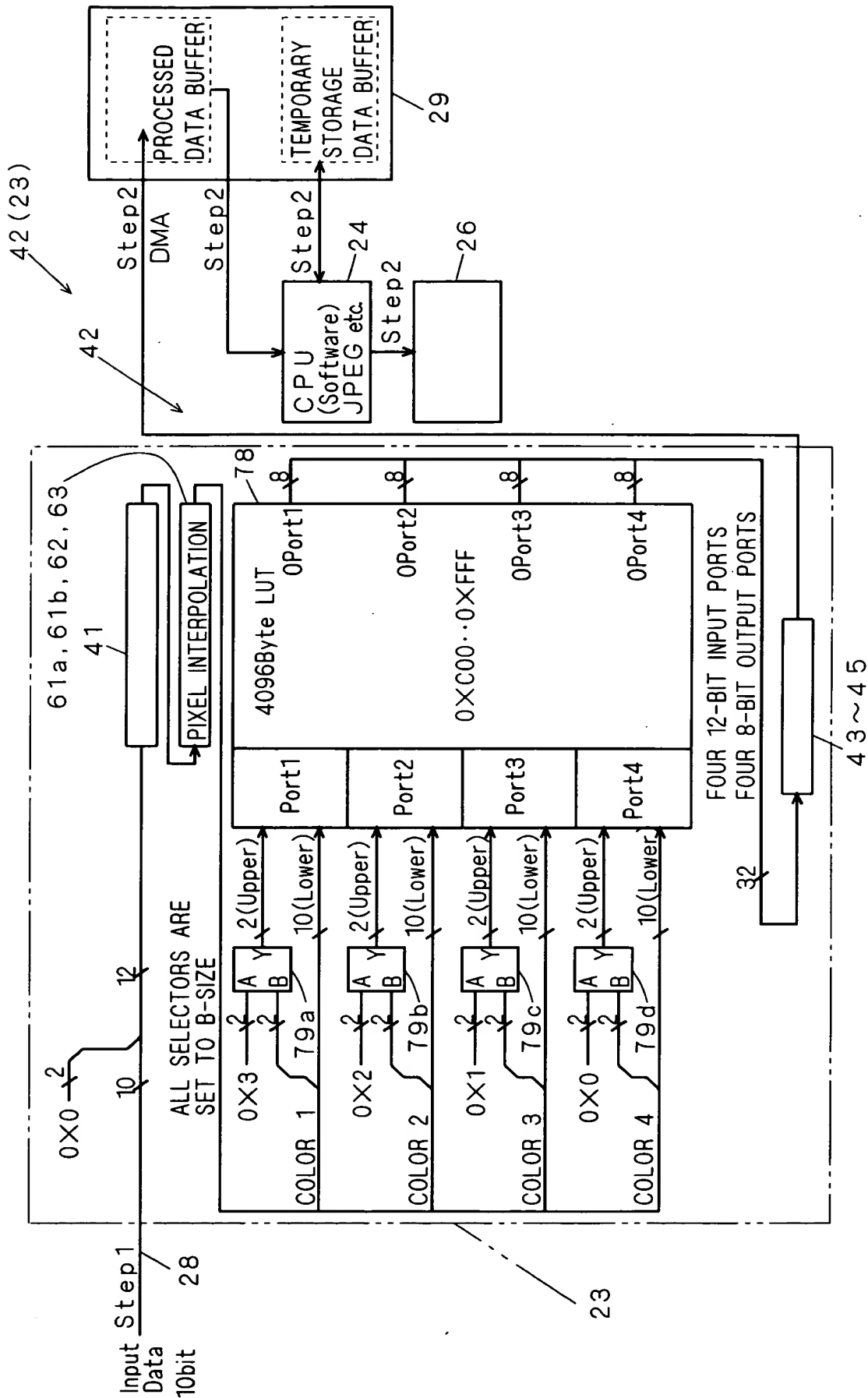
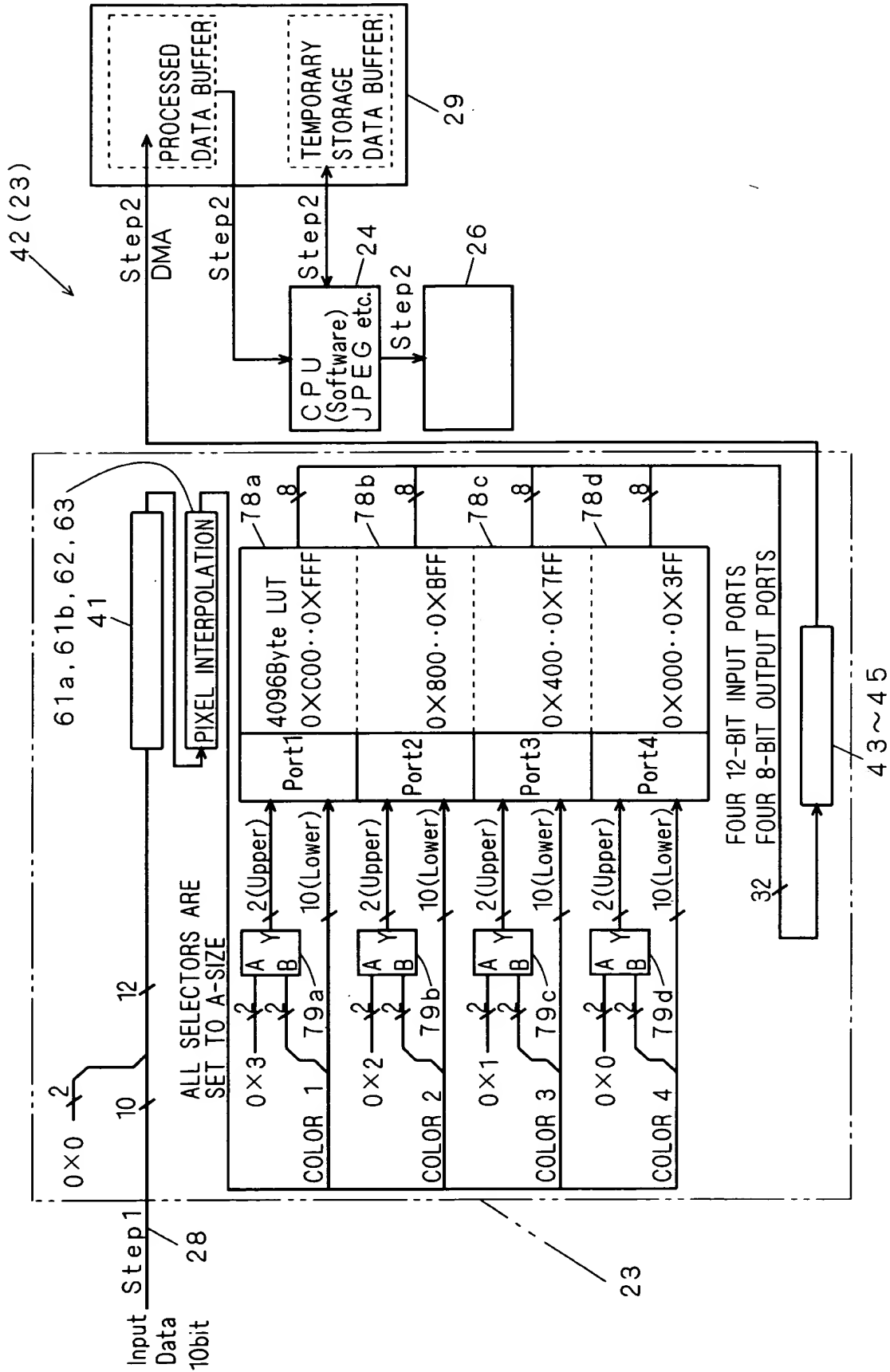
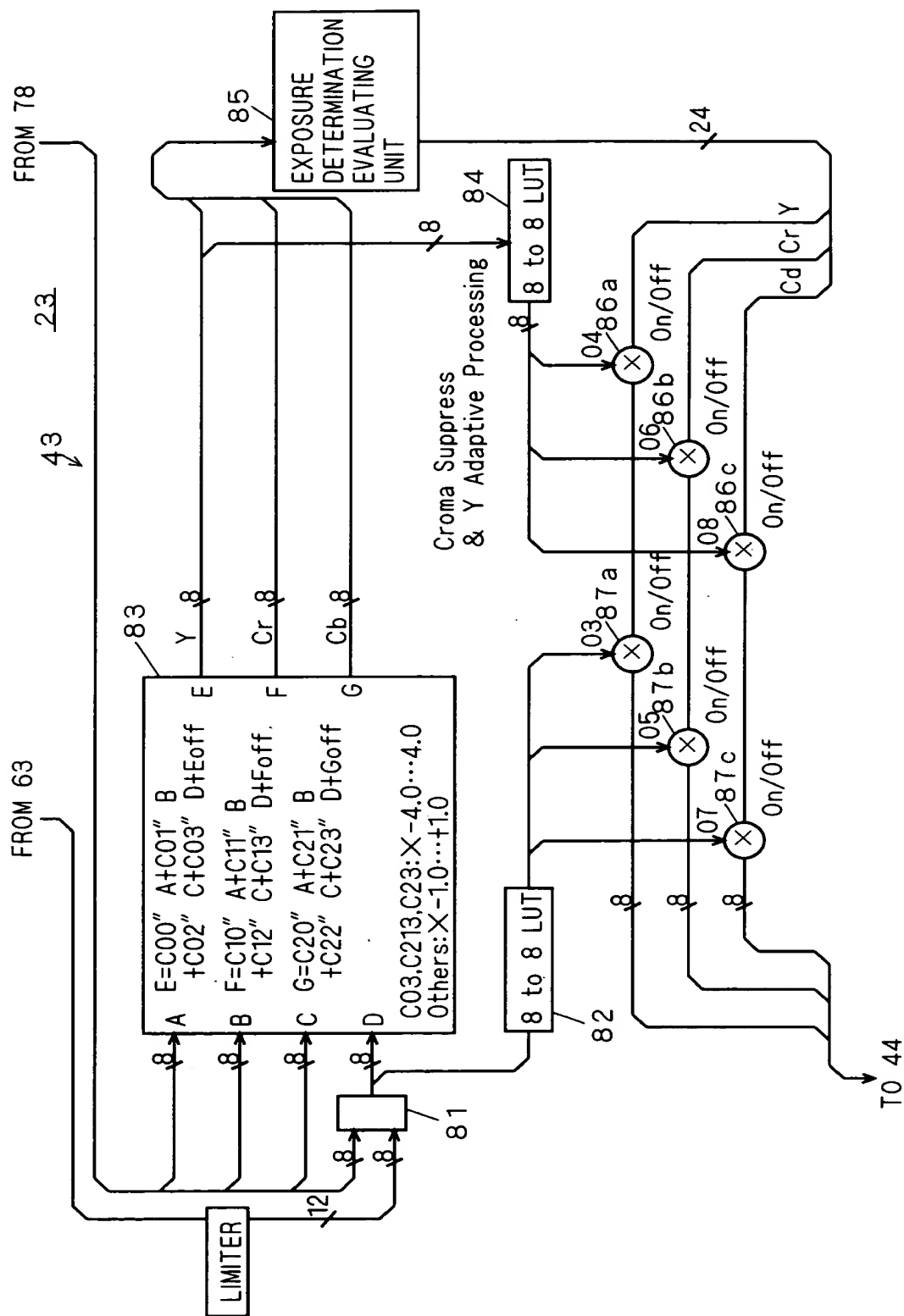


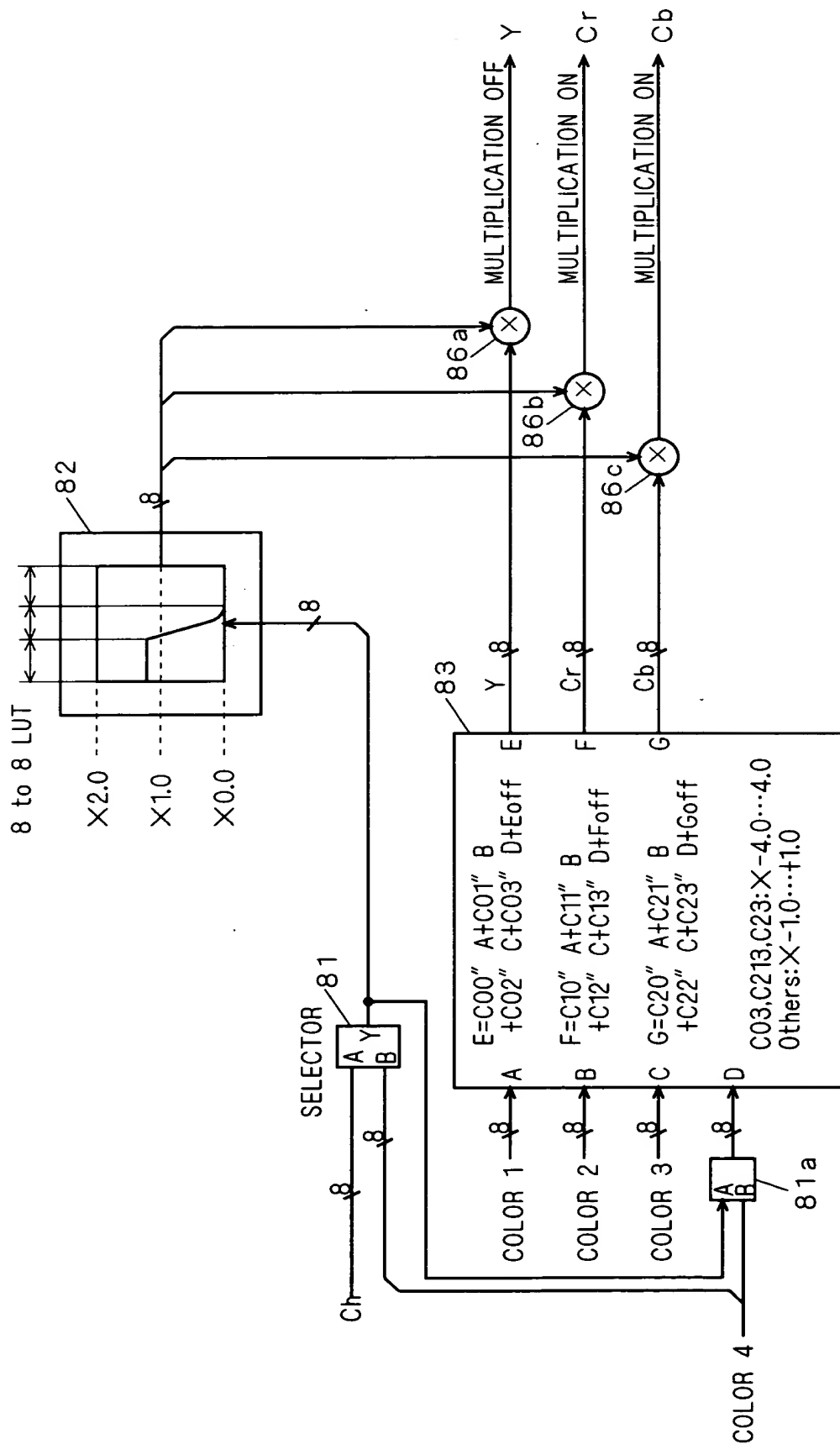
FIG. 18



# DRAFT



100-443887-100





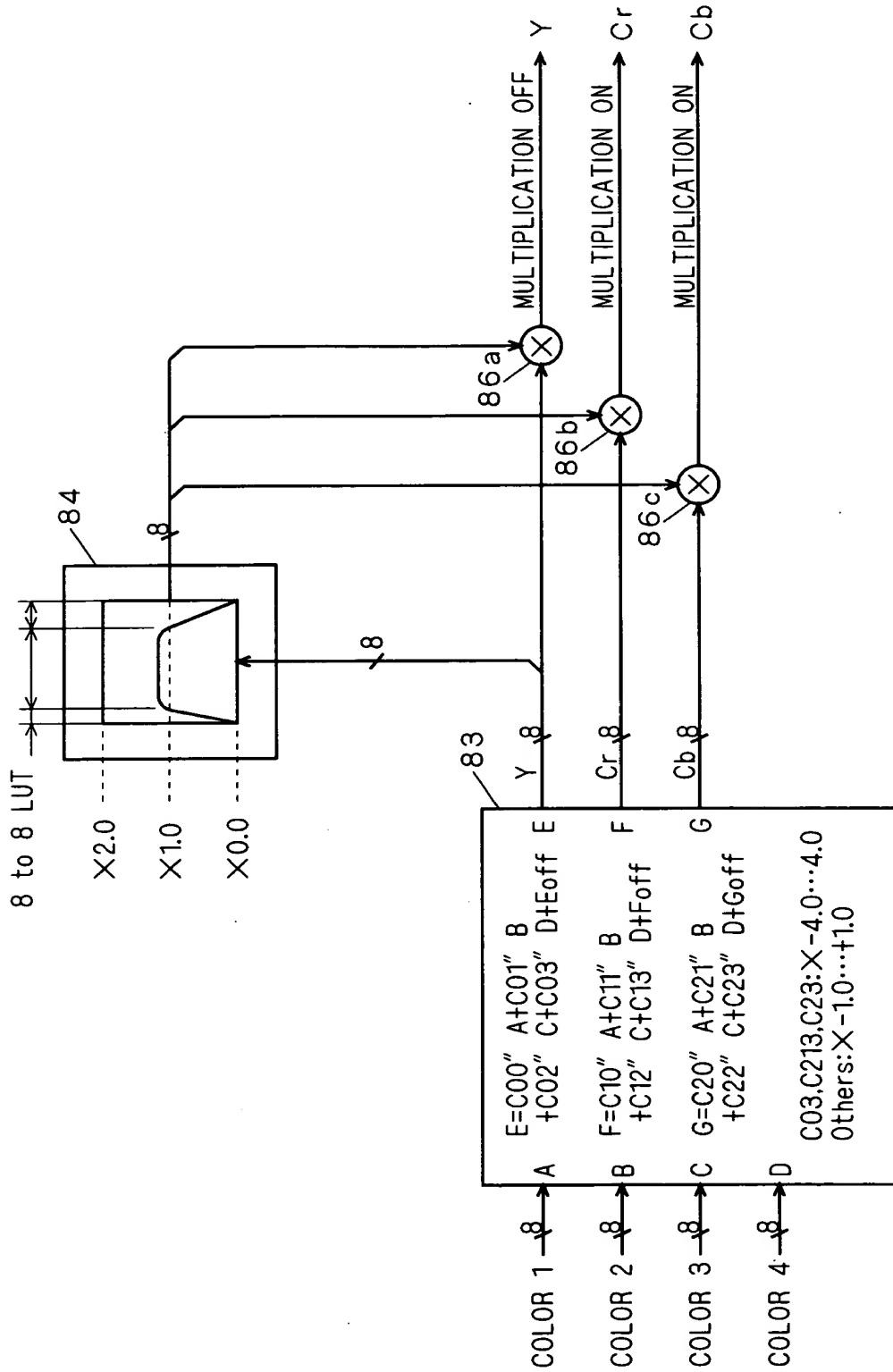


FIG. 22

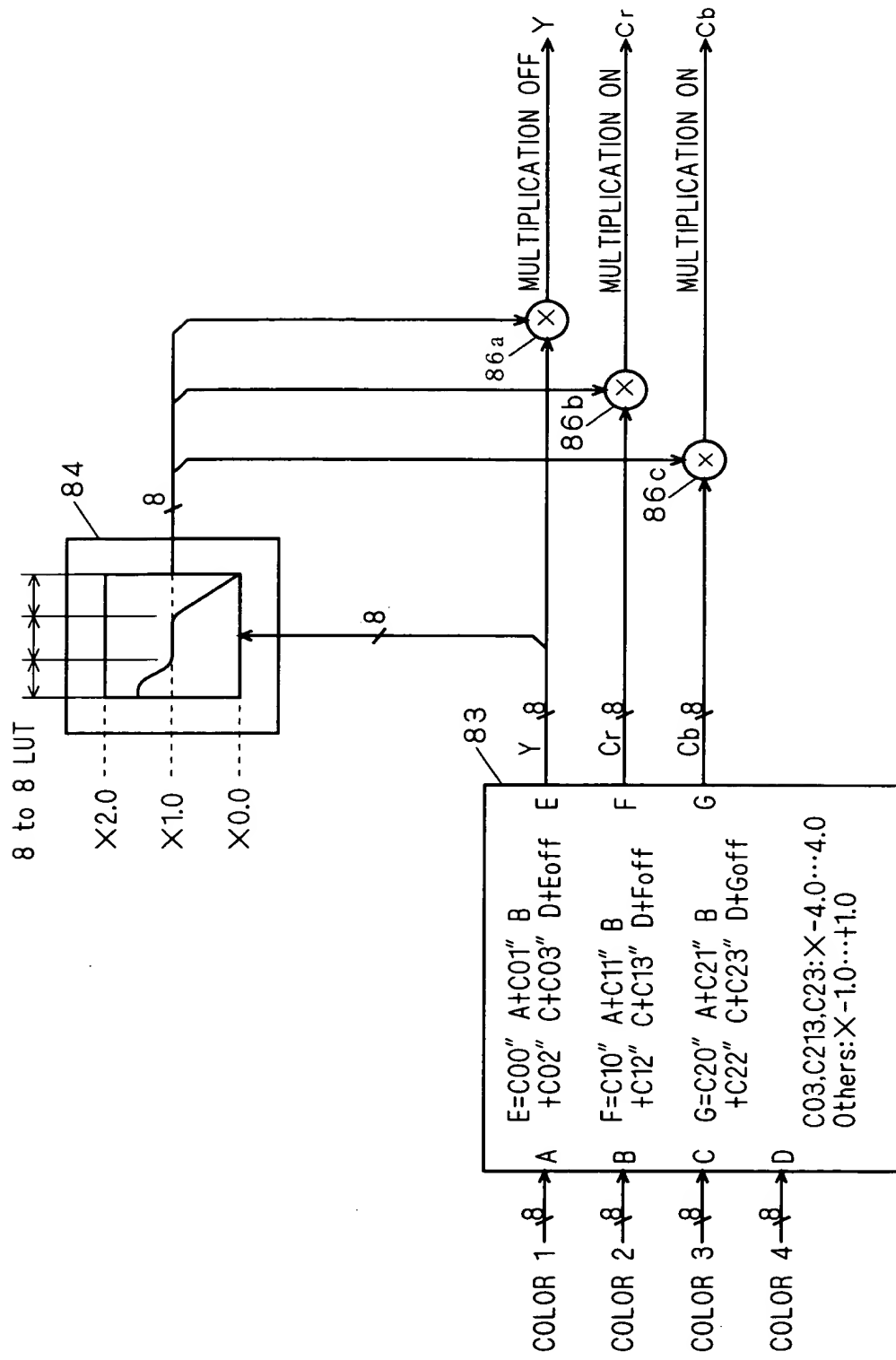
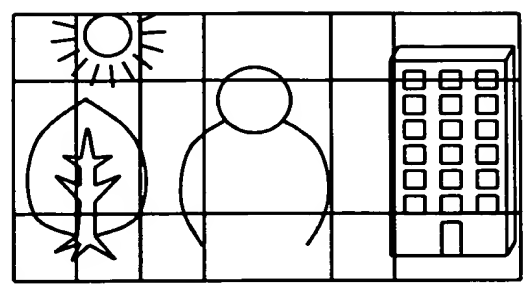
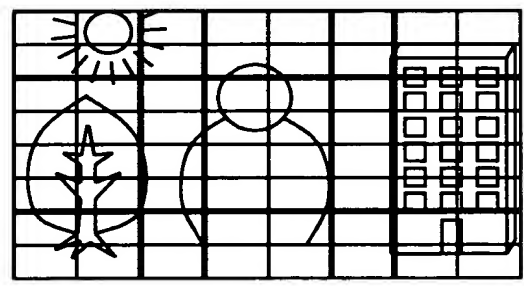


FIG. 23



— ARBITRARY BOUNDARY BLOCK

FIG. 24



— OPTIMIZED BLOCK  
— EQUALLY SPACED BLOCK

0459574.1239

FIG. 25

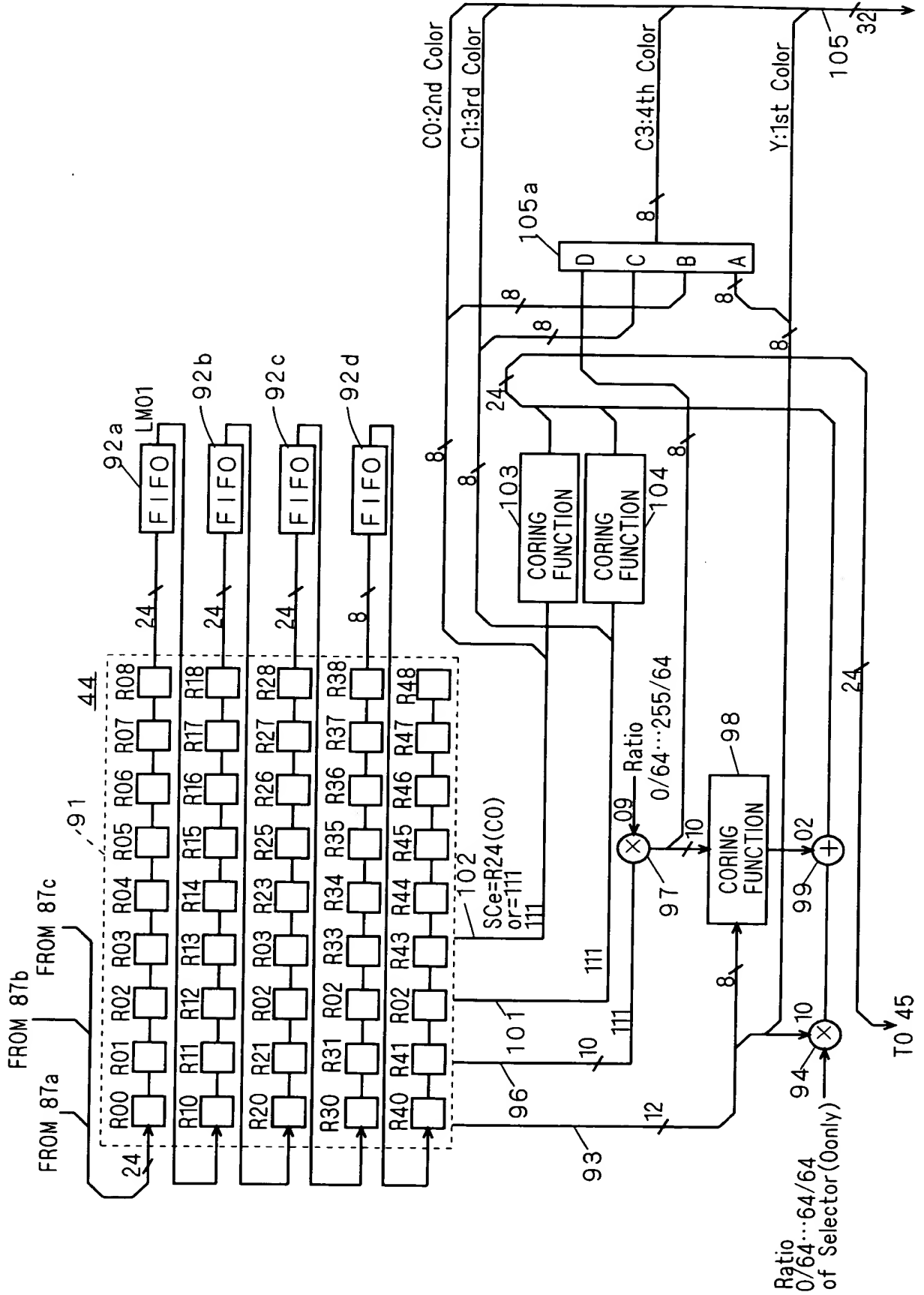


FIG. 26

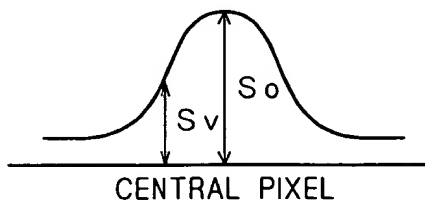
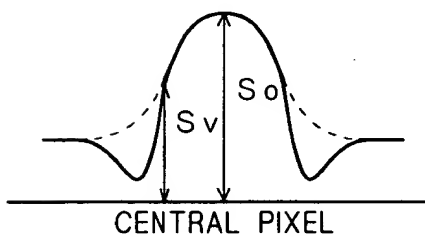


FIG. 27



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SHEET 12565460

FIG. 28

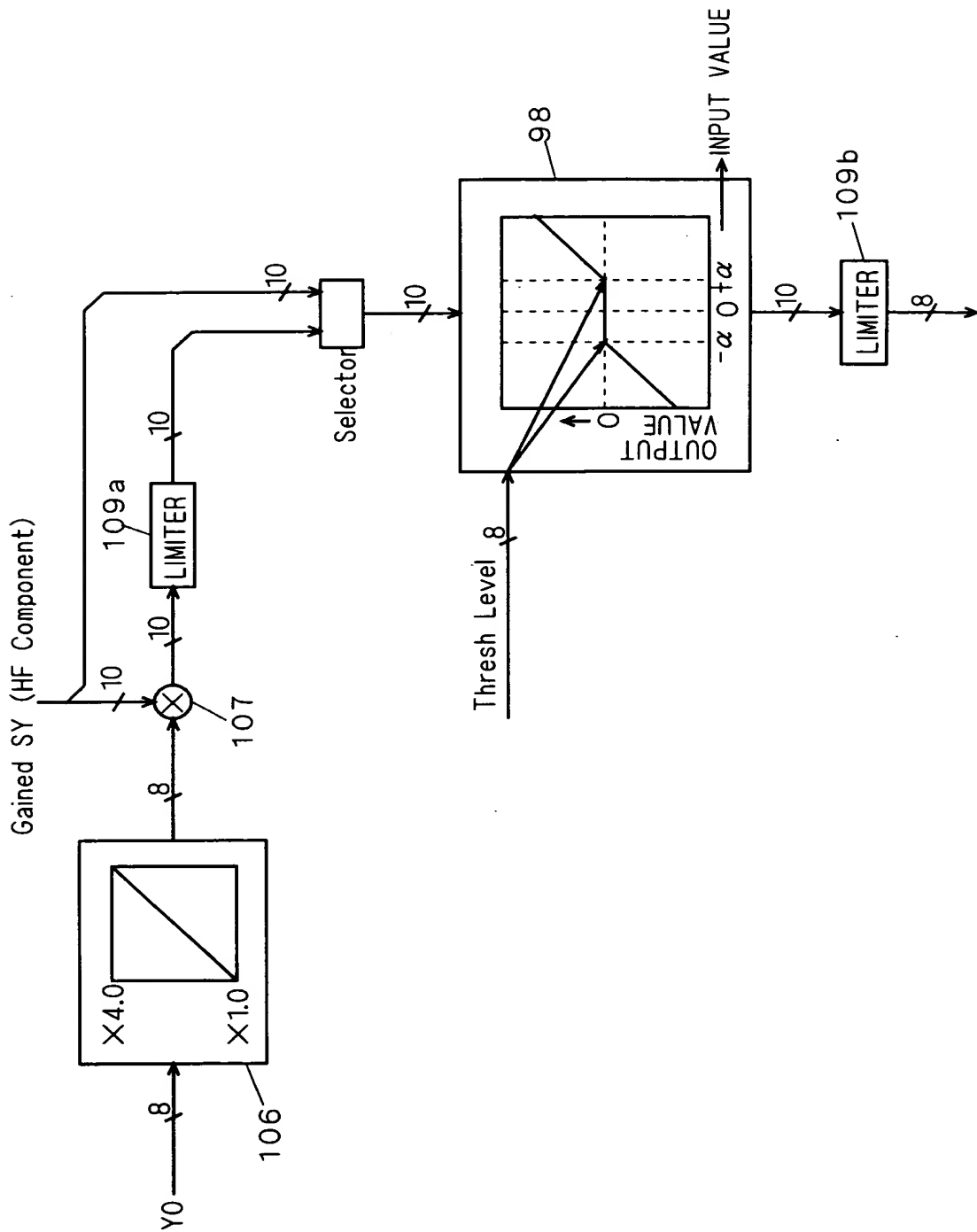


FIG. 29

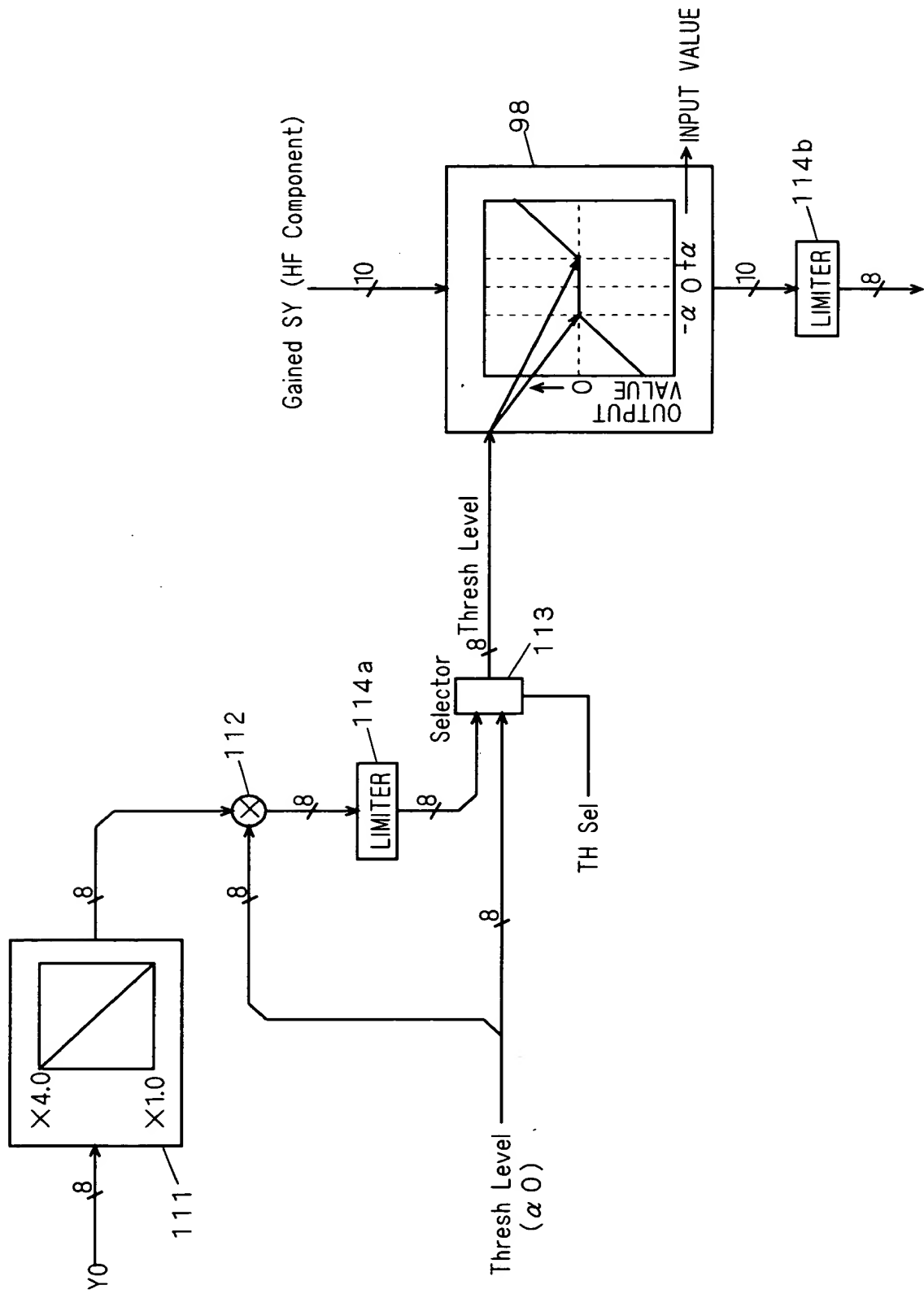


FIG. 30

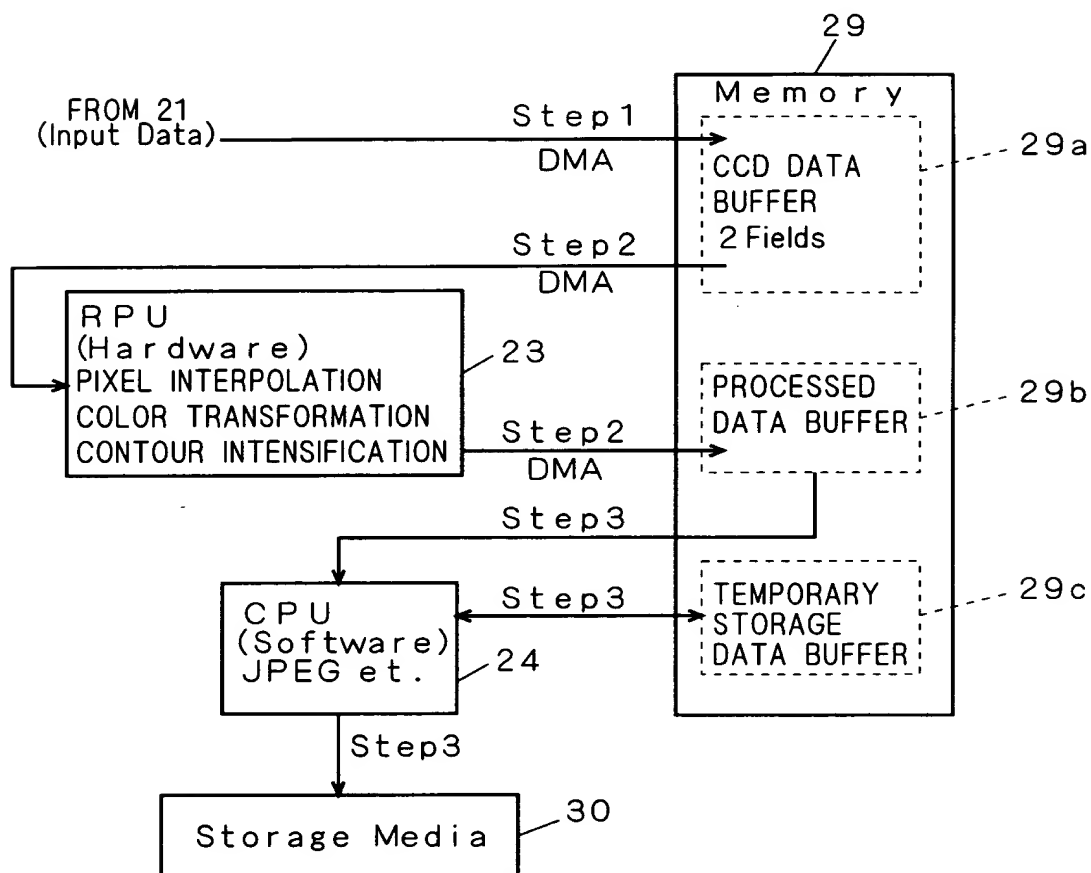




FIG. 31

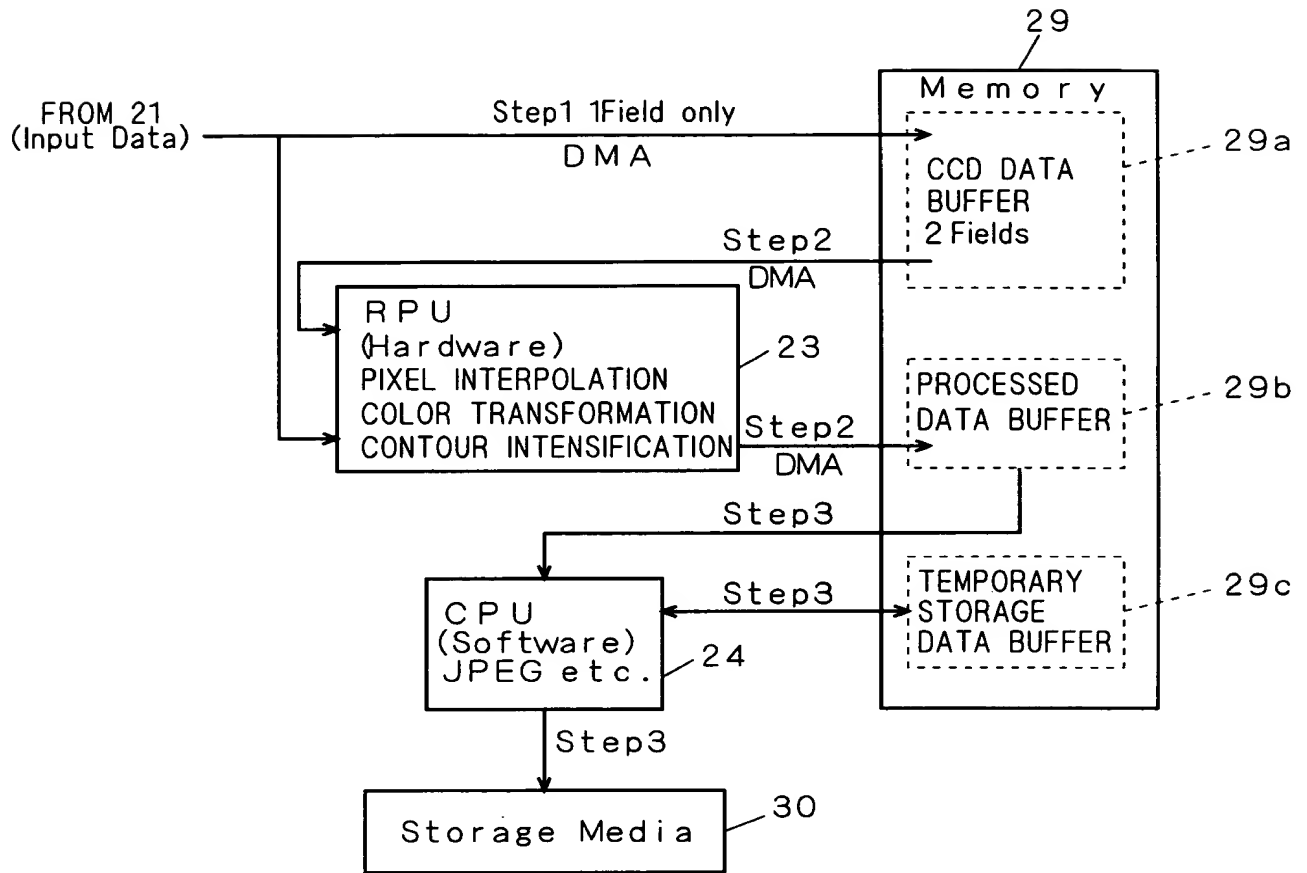


FIG. 32

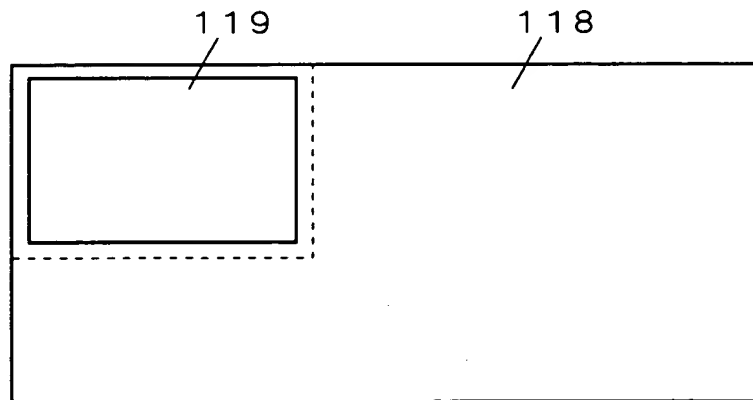


FIG. 33 (PRIOR ART)

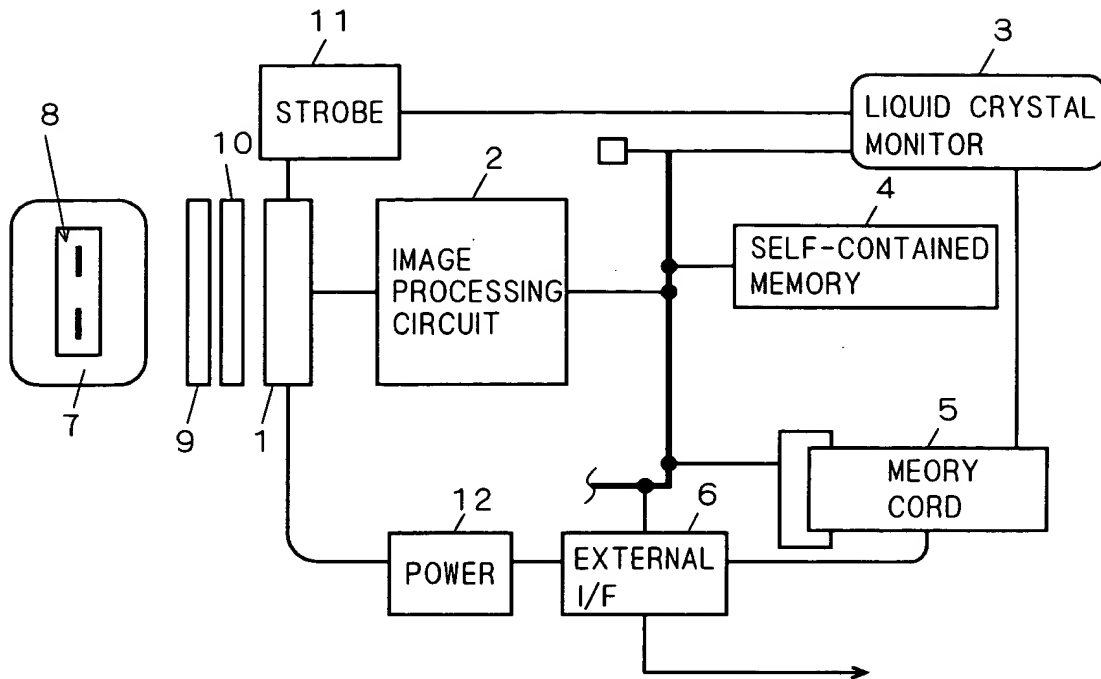


FIG. 34 (PRIOR ART)

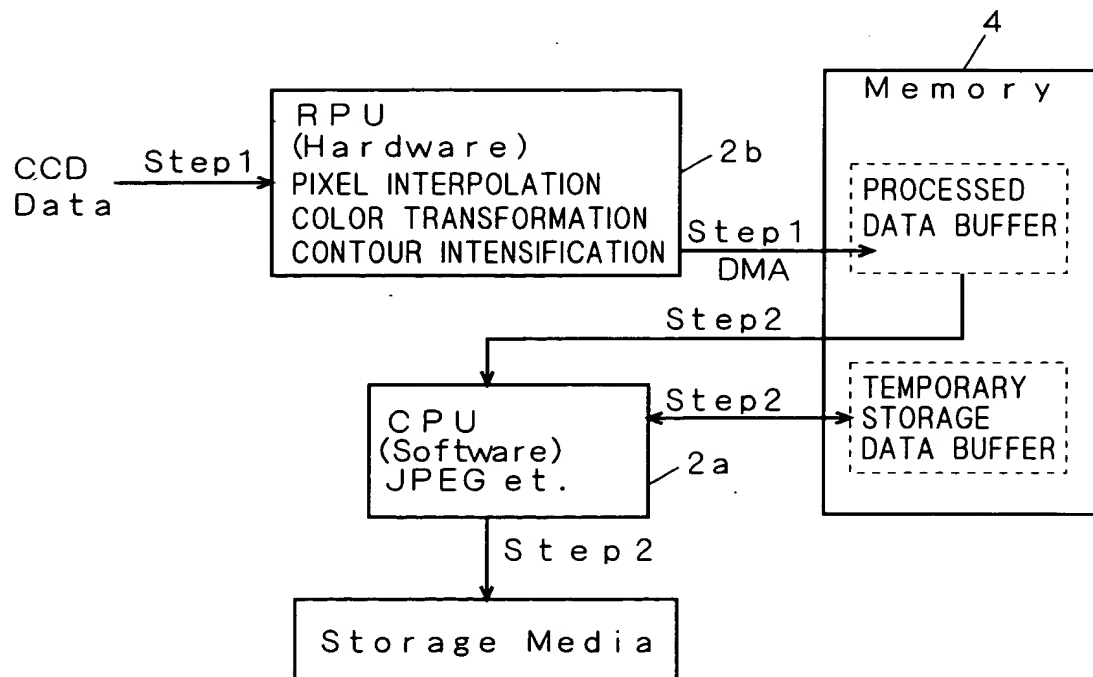


FIG. 35 (PRIOR ART)

